OPTIMISM IT SOLUTIONS

Raising leaders for the future

Professional, job oriented training programs

Advanced Java

Servlet
JSP
Javamail API
Junit
Maven
Struts 2
Working with IDE
Working with servers

SoC Design & Verification

Soc Design Architecture
Usecase listing down
Testcase coding
Testcase debug
GLS setup & debug
Vector setup & debug

Mechanical

AutoCAD
NX
CATIA
HyperMesh
HyperMesh advanced
Teamcenter
Ansys
NS

Other courses

IOS
Android OS
Angular
React JS

SAP

Functional / technical SAP HANA
ABAP
MM
WM
SD
BASIS
SOLMAN

VLSI

VLS Flow
Soc Design and verification concepts
Advanced Digital Design concepts
Verilog for Design and Verification
SystemVerilog for Advanced Verification
Verification IP Development
AsIC Verification concept
Essential UVM
RTL Debug
UNIX
PERL/Python Scripting

Python

Conditional Statements
Looping
Control Statements
String Manipulation
Lists
Tuple
Dictionaries
Functions
Modules
Input-Output
Exception Handling

Advance Python

OOPs concept
Regular expressions
CGI
Database
Networking
Multithreading
GUI Programming
Sending email

Embedded Systems

Linux
Embdedded C

1. Introduction to Embedded C Program
2. Basics of Embedded C Program
   - Keywords in Embedded C
   - Data Types in Embedded C

3.Structure of an Embedded C Program
4. Different Components of an Embedded C Program
   - Preprocessor Directive
   - Global Variables
   - Main Function

5. Embedded C Compiler

RTOS
Device drivers
Interfacing
Microcontroller ARM LPC2148
IoT on Raspberry Pi
Live projects

Physical Design

Design representation
VLSI design styles
VLSI physical design automation
Partitioning
Floorplanning
Floorplanning algorithms
Pin assignment
Placement
Grid routing
Detailed routing
Clock design
Clock network synthesis
Power and ground routing
Time closure
Timing driven placement
Physical synthesis
Built-in self test
Design rule check
Layout compaction
Test pattern generation
Design for testability
Boundary scan standard
Low power VLSI design
Interconnect modeling
Techniques to reduce power
Gate level design for low power
Other low power design techniques
Performance-driven design flow
Different approaches to timing optimization
Algorithm level techniques for low power design

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